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PPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/517,541	03/02/2000	Kia Silverbrook	AUTH15US	4978
7590 10/08/2003			EXAMINER	
Kia Silverbrook Silverbrook Research Pty Ltd 393 Darling Street			NGUYEN, NGA B	
			ART UNIT	PAPER NUMBER
Balmain, 2041			3628	
AUSTRALIA			DATE MAILED: 10/08/2003	
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Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)				
_	09/517,541	SILVERBROOK ET AL.				
. Office Action Summary	Examiner	Art Unit				
	Nga B. Nguyen	3628				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period of Failure to reply within the set or extended period for reply will, by statute - Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b). Status	36(a). In no event, however, may a reply be tim y within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from to cause the application to become ABANDONE	rely filed s will be considered timely. the mailing date of this communication. O (35 U.S.C. § 133).				
1) Responsive to communication(s) filed on 02 I	March 2000					
2a)☐ This action is FINAL . 2b)☑ Th	is action is non-final.					
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213. Disposition of Claims						
4) Claim(s) 1-8 is/are pending in the application.						
4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-8</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/or election requirement.						
Application Papers						
9) The specification is objected to by the Examiner.						
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
11) The proposed drawing correction filed on is: a) approved b) disapproved by the Examiner.						
If approved, corrected drawings are required in reply to this Office action. 12) The oath or declaration is objected to by the Examiner.						
Priority under 35 U.S.C. §§ 119 and 120						
_						
13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of:						
1. Certified copies of the priority documents have been received.						
Certified copies of the priority documents have been received in Application No						
3. Copies of the certified copies of the priority documents have been received in this National Stage						
application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.						
14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).						
a) ☐ The translation of the foreign language provisional application has been received. 15)☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.						
Attachment(s)		•				
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) S. Patent and Trademark Office.	5) Notice of Informal P	(PTO-413) Paper No(s) atent Application (PTO-152)				

U.S. Patent and Trademark Office PTOL-326 (Rev. 04-01) Art Unit: 3628

DETAILED ACTION

1. This Office Action is the answer to the communication filed on March 2, 2000, which paper has been placed of record in the file.

2. Claims 1-8 are pending in this application.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 4. Claims 1-8 are rejected under 35 U.S.C. 102(e) as being anticipated by Silverbrook et al (hereinafter Silverbrook), U.S. Patent No. 6,442,525.

Regarding to claim 1, Silverbrook discloses in an authentication chip in which secret data is manipulated, a method of shielding manipulations of the secret data from observation, including the steps of:

operating non-flashing CMOS structures in the chip, in which pMOS and nMOS transistors are driven such that they do not have intermediate resistance simultaneously during a change of state of the CMOS structure, to manipulate the secret data (columns 205-206); and

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operating conventional CMOS inverters adjacent the non-flashing CMOS structures at the same time (columns 202-203).

Regarding to claim 2, Silverbrook discloses generating continuous circuit noise to drive the conventional CMOS inverters (columns 202-203).

Regarding to claim 3, Silverbrook discloses generating continuous circuit noise to a tamper detection line and driving the conventional CMOS structures from the tamper detection line (columns 203-204).

Regarding to claim 4, Silverbrook discloses driving the conventional CMOS multiple times faster than non-flashing CMOS (columns 208-209).

Regarding to claims 5-8, Silverbrook discloses an authentication chip for performing the method as discussed in claims 1-4 above, moreover, Silverbrook discloses a noise generator connected to the conventional CMOS inverters (see column 202, lines 54-67).

Conclusion

- 5. Claims 1-8 are rejected.
- 6. The prior arts made of record and not relied upon is considered pertinent to applicant's disclosure:

Brown et al (US 4,852,680) discloses vehicle anti-theft system sith remote security module contain a CMOS semiconductor.

Sano (US 5,998,842) discloses a MOS semiconductor device.

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Harari et al (6,149,316) discloses a flash EEPROM system contains a CMOS semiconductor.

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to examiner Nga B. Nguyen whose telephone number is (703) 306-2901. The examiner can normally be reached on Monday-Thursday from 9:00AM-6:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hyung S. Sough can be reached on (703) 308-0505.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 306-1113.

8. Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

C/o Technology Center 3600

Washington, DC 20231

Or faxed to:

(703) 872-9326 (for formal communication intended for entry),

or

(703) 308-3691 (for informal or draft communication, please label "PROPOSED" or "DRAFT").

Hand-delivered responses should be brought to Crystal Park 5, 2451 Crystal Drive, Arlington, VA, Seventh Floor (Receptionist).

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Nga B. Nguyen

MgaNguyen October 1, 2003